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1 [RO-cash: An efficient and practical recoverable pre-paid offline e-cash scheme using bilinear pairing](#)
 Wen-Sheng Jiang

April 2010

Journal of Systems and Software, Volume 83 Issue 4**Publisher:** Elsevier Science Inc.**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a,

Nowadays, various electronic commerce services can be found and are widely used in the Internet. The mature of information and network technologies. A practical and flexible electronic payment system is key success factors in electronic ...

Keywords: Anonymity, Bilinear pairing, Coin recovery, Elliptic curve cryptosystem, Offline electronic payment system, Smart card

2 [A taxonomy for key escrow encryption systems](#)

Dorothy E. Denning, Dennis K. Branstad

March 1996

Communications of the ACM, Volume 39 Issue 3**Publisher:** ACM [Request Permissions](#)Full text available: [PDF](#) (548.67 KB)**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 88, Downloads (Overall): 908, Cited by: 10

3 [PicoDBMS: Scaling down database techniques for the smartcard](#)

Philippe Pucheral, Luc Bougnim, Patrick Valduriez, Christophe Bobineau

September 2001 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 11 Issue 3**Publisher:** Springer-Verlag New York, Inc.Full text available: [PDF](#) (259.03 KB)**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 49, Downloads (Overall): 736, Cited by: 10

Smartcards are the most secure portable computing device today. They have been used successfully in many applications involving money, and proprietary and personal data (such as banking, healthcare, insurance, etc). They are getting more powerful (with ...)

Keywords: Atomicity, Durability, Execution model, PicoDBMS, Query optimization, Smartcard applications, Storage model

4 [Low power clock recovery circuit for passive HF RFID tag](#)

Fan Bo, Dai Yuli, Zhang Xiaoxing, Lu Yingjie

May 2009 **Analog Integrated Circuits and Signal Processing**, Volume 59 Issue 2**Publisher:** Kluwer Academic Publishers**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a,

A low power clock recovery circuit for passive HF RFID tag is presented. The proposed clock recovery circuit, based on the architecture of Phase Locked Loop (PLL), is used to generate a stable system clock when the tag occurs from interrogator ...

Keywords: 100% ASK modulation, Clock recovery, HF RFID tag, Low power, PLL

5 [Dictatorial Transaction Processing: Atomic Commitment Without Veto Right](#)

Maha Abdallah, Rachid Guerraoui, Philippe Pucheral

May 2002 **Distributed and Parallel Databases**, Volume 11 Issue 3**Publisher:** Kluwer Academic PublishersFull text available: [PDF](#) [Publisher Site](#)**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a,